

Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	10/759483
				Filing Date	1/16/2004
				First Named Inventor	Rodney E. Hooker
				Group Art Unit	2186
				Examiner Name	Ryan Dare
				Attorney Docket Number	CNTR.2232
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U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
RD		6622211 (English-Language Equivalent of Chinese application CN1397887A published 02-19-2003)	9-16-2003	Henry et al.	

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RD		CN1391167A (US20030159003A1) provided herewith as an English- Language Equivalent Application)	01-15-2003	Geisking et al.		

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RD		GB2307319	05-21-1997	Han Heung Kim		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
RD		"Dual On-Chip Instruction Cache Organization in High Speed Processors." IBM Technical Disclosure Bulletin. IBM Corp. New York, US. Volume 37, No. 12, December 1994, pages 213-214.	

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RD		GB2260429	04-14-1993	Kagan et al.		

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RD		5500950	03-19-1996	Becker et al.	
RD		5751990	05-12-1998	Krolak et al.	
RD		5956752	09-21-1999	Gregory S. Mathews	
RD		6275903	08-14-2001	Koppala et al.	

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